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NOTICE OF ALLOWANCE AND FEE(S) DUE

65913 7590 07/25/2008

NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131

EXAMINER				
MERANT, GUERRIER				
ART UNIT	PAPER NUMBER			

2117 DATE MAILED: 07/25/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/539.104	06/15/2005	Otto Steinbusch	US02 0610 US2	9255

TITLE OF INVENTION: CONNECTING MULTIPLE TEST ACCESS PORT CONTROLLERS ON A SINGLE TEST ACCESS PORT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	10/27/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or <u>Fax</u> (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where ar in m

maintenance fee notifica	correspondence includir ed below or directed off titions. DENCE ADDRESS (Note: Use BI		Not Fee pap	e: A certificate of (s) Transmittal. Th ers. Each additiona	mailing is certiful paper	g can only be used for ficate cannot be used for the such as an assignment	correspondence address as rate "FEE ADDRESS" for r domestic mailings of the or any other accompanying at or formal drawing, must
65913 7590 07/25/2008 NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ			hav I he Stal	Certificate creby certify that these Postal Service verseed to the Mai	e of ma rtificate his Fee(with suf 1 Stop	iling or transmission. e of Mailing or Transi	nission deposited with the United t class mail in an envelope
1109 MCKAY I SAN JOSE, CA							(Depositor's name)
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							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTC	RNEY DOCKET NO.	CONFIRMATION NO.
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nonprovisional	NO	\$1440	\$300	\$0		\$1740	10/27/2008
EXAM	IINER	ART UNIT	CLASS-SUBCLASS]			
MERANT,	GUERRIER	2117	714-747000	J			
"Fee Address" ind PTO/SB/47; Rev 03-(Number is required. 3. ASSIGNEE NAME A PLEASE NOTE: Un	ND RESIDENCE DATA less an assignee is ident th in 37 CFR 3.11. Comp	"Indication form led. Use of a Customer A TO BE PRINTED ON This ified below, no assignee	(1) the names of up to or agents OR, alternati (2) the name of a single registered attorney or 2 registered patent attolisted, no name will be THE PATENT (print or tyle data will appear on the pT a substitute for filing an (B) RESIDENCE: (CITY	vely, le firm (having as a agent) and the nam rneys or agents. If printed. pe) latent. If an assign assignment.	n members of union no nan	p to p to a e is 3dentified below, the do	ocument has been filed for
Please check the appropri	riate assignee category or	categories (will not be pr	rinted on the patent):	Individual 🗖 C	orporat	ion or other private gro	up entity Government
4a. The following fee(s) are submitted: Issue Fee Publication Fee (No small entity discount permitted) Advance Order - # of Copies 5. Change in Entity Status (from status indicated above)			4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) A check is enclosed. Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number (enclose an extra copy of this form).				
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NOTE: The Issue Fee an interest as shown by the	nd Publication Fee (if req records of the United Sta	uired) will not be accepte tes Patent and Trademark	d from anyone other than t				e assignee or other party in
Authorized Signature				Date			
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This collection of inform an application. Confiden submitting the complete this form and/or suggest Box 1450, Alexandria, V Alexandria, Virginia 223	tiality is governed by 35 d application form to the ions for reducing this bu. Virginia 22313-1450. DC	CFR 1.311. The informatic U.S.C. 122 and 37 CFR USPTO. Time will vary rden, should be sent to the ONOT SEND FEES OR	on is required to obtain or 1.14. This collection is esto depending upon the individence Chief Information Offic COMPLETED FORMS T	retain a benefit by timated to take 12 vidual case. Any co er, U.S. Patent and O THIS ADDRES:	the pub minutes ommen Trader S. SEN	lic which is to file (and s to complete, includin ts on the amount of tir nark Office, U.S. Depa D TO: Commissioner f	by the USPTO to process) g gathering, preparing, and ne you require to complete rtment of Commerce, P.O. for Patents, P.O. Box 1450,

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

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10/539,104	06/15/2005	Otto Steinbusch	US02 0610 US2	9255
65913 7	590 07/25/2008		EXAM	INER
NXP, B.V.			MERANT, O	GUERRIER
	ΓUAL PROPERTY DE	PARTMENT	ART UNIT	PAPER NUMBER
M/S41-SJ 1109 MCKAY DF SAN JOSE, CA 9:			2117 DATE MAILED: 07/25/200	8

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 85 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 85 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)		
	10/539,104	STEINBUSCH, OTTO		
Notice of Allowability	Examiner	Art Unit		
	Guerrier Merant	2117		
The MAILING DATE of this communication appe	ears on the cover sheet wi	th the correspondence address		
All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	or other appropriate commu GHTS. This application is s	inication will be mailed in due course		
1. This communication is responsive to <u>03/05/08</u> .				
2. ☑ The allowed claim(s) is/are <u>1-15</u> .				
 Acknowledgment is made of a claim for foreign priority ur a) ☐ All b) ☐ Some* c) ☐ None of the: 	nder 35 U.S.C. § 119(a)-(d)	or (f).		
 Certified copies of the priority documents have 	been received.			
2. Certified copies of the priority documents have	been received in Application	n No		
3. Copies of the certified copies of the priority do	cuments have been received	d in this national stage application fro	om the	
International Bureau (PCT Rule 17.2(a)).				
* Certified copies not received:				
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirem	ents	
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			E OF	
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.			
(a) ☐ including changes required by the Notice of Draftspers	on's Patent Drawing Review	v (PTO-948) attached		
1) ☐ hereto or 2) ☐ to Paper No./Mail Date				
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or	in the Office action of		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			of	
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			ne	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 ☐ Notice of In	formal Patent Application		
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413),		
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./	Mail Date Amendment/Comment		
Paper No./Mail Date4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's	Statement of Reasons for Allowance	e	
of Biological Material	9.			
	/Jacques Louis-Ja	cques/, AU 2117, 7/7/08		

DETAILED ACTION

EXAMINER'S AMENDMENT

- 1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 2. The application has been amended as follows:

Insert in page 1, line 1: BACKGROUND OF THE INVENTION

Insert in page 2, line 1: SUMMARY OF THE INVENTION

Insert in page 2, line 9: DETAILED DESCRIPTION OF THE DRAWINGS

Insert in page 2, line 25: DESCRIPTION OF THE PREFERRED

EMBODIMENTS.

Reasons For Allowance

- 3. The following is an examiner's statement of reasons for allowance:
 - The present invention includes:
 - A) A method of coupling a plurality of test access port (TAP) controllers to a single external interface, comprising:
 - a) resetting a first bit in each of a plurality of TAP controllers to a known state;
 - b) producing a first signal based, at least in part, on the state of the first bit in each of the plurality of TAP controllers;

c) selecting one of the plurality of TAP controllers based, at least in part, on the first signal;

- d) coupling an external input terminal to an input terminal of the selected one of the plurality of TAP controllers; and
- e) coupling an output terminal of the selected one of the plurality of TAP controllers to an external output terminal.

B) An integrated circuit, comprising:

a plurality of functional blocks, each functional block having a test access port (TAP) controller coupled thereto;

each TAP controller including a first register bit, each first register bit adapted to produce a known output state in response to a reset signal, each first register bit further adapted to toggle in response to a register write operation;

and routing logic adapted to selectively provide, based at least in part on the state of the plurality of first register bits, a communication path between an external input signal source and an input terminal of a selected one of the TAP controllers, the routing logic further adapted to provide, in response to toggling of the first register bit in the selected TAP controller and based on the state of each of the first register bits, a communication path between the external input signal source and an input terminal of a second one of the TAP controllers.

The prior arts of record teach:

C) A method of coupling a plurality of test access port (TAP) controller to a single external interface, comprising:

- a) resetting a first bit in a master TAP controllers to a known state;
- b) producing a first signal based, at least in part, on the state of the first bit in the master TAP controller;
- c) selecting one of the plurality of TAP controllers based, at least in part, on the first signal;
- d) coupling an external input terminal to an input terminal of the selected one of the plurality of TAP controllers; and
- e) coupling an output terminal of the selected one of the plurality of TAP controllers to an external output terminal.

D) An integrated circuit, comprising:

a plurality of functional blocks, each functional block having a test access port (TAP) controller coupled thereto;

A master TAP controller including a first register bit adapted to produce a known output state in response to a reset signal, the first register bit further adapted to toggle in response to a register write operation;

and routing logic adapted to selectively provide, based at least in part on the state of the first register bits, a communication path between an external input signal source and an input terminal of a selected one of the TAP controllers. Art Unit: 2117

However, the prior art of records fail to teach resetting a first bit in each of a plurality of TAP controllers to a known state, producing a first signal based, at least in part, on the state of the first bit in each of the plurality of TAP controllers and selecting one of the plurality of TAP controllers based, at least in part, on the first signal. Also, the prior arts of record fail to teach that each TAP controller including a first register bit, each first register bit adapted to produce a known output state in response to a reset signal, each first register bit further adapted to toggle in response to a register write operation; and routing logic adapted to selectively provide, based at least in part on the state of the plurality of first register bits, a communication path between an external input signal source and an input terminal of a selected one of the TAP controllers, the routing logic further adapted to provide, in response to toggling of the first register bit in the selected TAP controller and based on the state of each of the first register bits, a communication path between the external input signal source and an input terminal of a second one of the TAP controllers.

Therefore, the prior arts of record do not anticipate or render obvious the claimed inventions.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2117

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Exr. Merant Guerrier whose telephone number is (571) 270-1066. The examiner can normally be reached Monday through Thursday from 10: 30 a.m. to 3:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques Louis Jacques, can be reached on (571) 272-6962. Draft or Informal faxes, which will not be entered in the application, may be submitted directly to the examiner at (571) 270-2066.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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Guerrier Merant 06/26/08

/JACQUES H LOUIS-JACQUES/ Supervisory Patent Examiner, Art Unit 2100